

<b>STUDY MODULE DESCRIPTION FORM</b>		
Name of the module/subject <b>Bases of electronics and the telecommunications</b>		Code <b>1010331421010327054</b>
Field of study <b>Information Engineering</b>	Profile of study (general academic, practical) <b>(brak)</b>	Year /Semester <b>1 / 2</b>
Elective path/specialty <b>-</b>	Subject offered in: <b>polish</b>	Course (compulsory, elective) <b>obligatory</b>
Cycle of study: <b>First-cycle studies</b>	Form of study (full-time, part-time) <b>full-time</b>	
No. of hours Lecture: <b>1</b> Classes: <b>-</b> Laboratory: <b>1</b> Project/seminars: <b>-</b>		No. of credits <b>3</b>
Status of the course in the study program (Basic, major, other) <b>(brak)</b>		(university-wide, from another field) <b>(brak)</b>
Education areas and fields of science and art <b>technical sciences</b> <b>Technical sciences</b>		ECTS distribution (number and %) <b>3 100%</b> <b>3 100%</b>
<b>Responsible for subject / lecturer:</b>  Prof. dr hab. inż. Konrad Skowronek email: konrad.skowronek@put.poznan.pl tel. 616652388 Elektryczny ul. Piotrowo 3A, 60-965 Poznań		
<b>Prerequisites in terms of knowledge, skills and social competencies:</b>		
1	<b>Knowledge</b>	Basic knowledge of mathematics, physics and electrical engineering basics.
2	<b>Skills</b>	The ability to understand and interpret knowledge conveyed in the classroom. Ability to effectively self-education in a field related to the chosen field of study.
3	<b>Social competencies</b>	Is aware of the need to broaden their competence, willingness to work together as a team.
<b>Assumptions and objectives of the course:</b> Knowledge of basic electronic circuits and systems in telecommunications. Knowledge of methods of analysis and synthesis of telecommunications systems.		
<b>Study outcomes and reference to the educational results for a field of study</b>		
<b>Knowledge:</b>		
1. Characterize the principles modeling of electronic systems and telecommunications. - [K_W02 +++, K_W03 ++]		
2. Construct basic electromagnetic devices, electronics and telecommunications. - [K_W03 ++]		
<b>Skills:</b>		
1. Apply knowledge of electrical circuit theory, electronics and telecommunications, necessary to determine the relevant parameters of electromagnetic circuits and systems. - [K_U08 ++]		
2. Obtain information from the literature and the Internet, work individually, independently solve problems in the theory of analysis and modeling of electrical, electronics and telecommunications. - [K_U01 ++, K_U03 +]		
<b>Social competencies:</b>		
1. Aware of a gravity of the teamwork and the responsibility for projects together carried out at preserving notation standards and of linguistic correctness. - [K_K04+++, K_K07++]		
<b>Assessment methods of study outcomes</b>		

<p>Lecture:          ? assess the knowledge and skills listed on the written test of the theory of electronics and telecommunications.</p> <p>Laboratory:          ? to evaluate the skills to prepare the measurement circuitry and communication - skills check for each class and one test during the semester.</p> <p>Get extra points for the activity in the classroom, and in particular for:          ? propose to discuss additional aspects of the subject;          ? the effectiveness of the application of the knowledge gained during solving the given problem;          ? ability to work within a team practice performing the task detailed in the laboratory;          ? subsequent to the improvement of teaching materials;          ? developed aesthetic diligence reports and jobs - in the self-study.</p>		
<b>Course description</b>		
<p>Basic concepts for electronic circuits and systems, and telecommunications. Standards and Regulation. Designing circuits and systems. Basic knowledge of operating systems and communications channel.</p>		
<b>Basic bibliography:</b>		
<p>1. Bolkowski S. "Teoria obwodów elektrycznych", WNT, Warszawa, 1998          2. Krakowski M. "Elektrotechnika Teoretyczna. T.1", PWN, Warszawa, 1995          3. Lurch E. "Podstawy Techniki Elektronicznej", PWN Warszawa          4. Wesołowski K. "Podstawy cyfrowych systemów telekomunikacyjnych", WKŁ, 2006</p>		
<b>Additional bibliography:</b>		
<p>1. Mikołajuk K., Trzaska Z. " Zbiór zadań z elektrotechniki teoretycznej", WNT, W-a, 1978          2. Chua L.O., Desoer C.A., Kuh E.S. "Linear and Nonlinear Circuits", McGraw-Hill Inc., 1987          3. Internet          4. Prace dyplomowe IAIII, IEEP</p>		
<b>Result of average student's workload</b>		
<b>Activity</b>	<b>Time (working hours)</b>	
1. participation in lecture classes	15	
2. participation in laboratory classes	15	
3. participation in consultation concerning the lecture	3	
4. participation in consultation concerning the laboratory	3	
5. preparation for the test/exam	24	
6. test/exam	2	
7. preparing the laboratory description	24	
<b>Student's workload</b>		
<b>Source of workload</b>	<b>hours</b>	<b>ECTS</b>
Total workload	86	3
Contact hours	38	1
Practical activities	42	2